



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 4076**
Yoshiharu ANDA et al. : Docket No. 2003_0955A
Serial No. 10/617,793 : Group Art Unit 2826
Filed July 14, 2003 : Examiner Tan N. Tran

SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement of February 17, 2004, Applicants hereby elect Group I, which is drawn to a semiconductor device and is embodied by claims 1-6.

Having made the required election, a full examination on the merits of the elected group is requested.

Respectfully submitted,

Yoshiharu ANDA et al.

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